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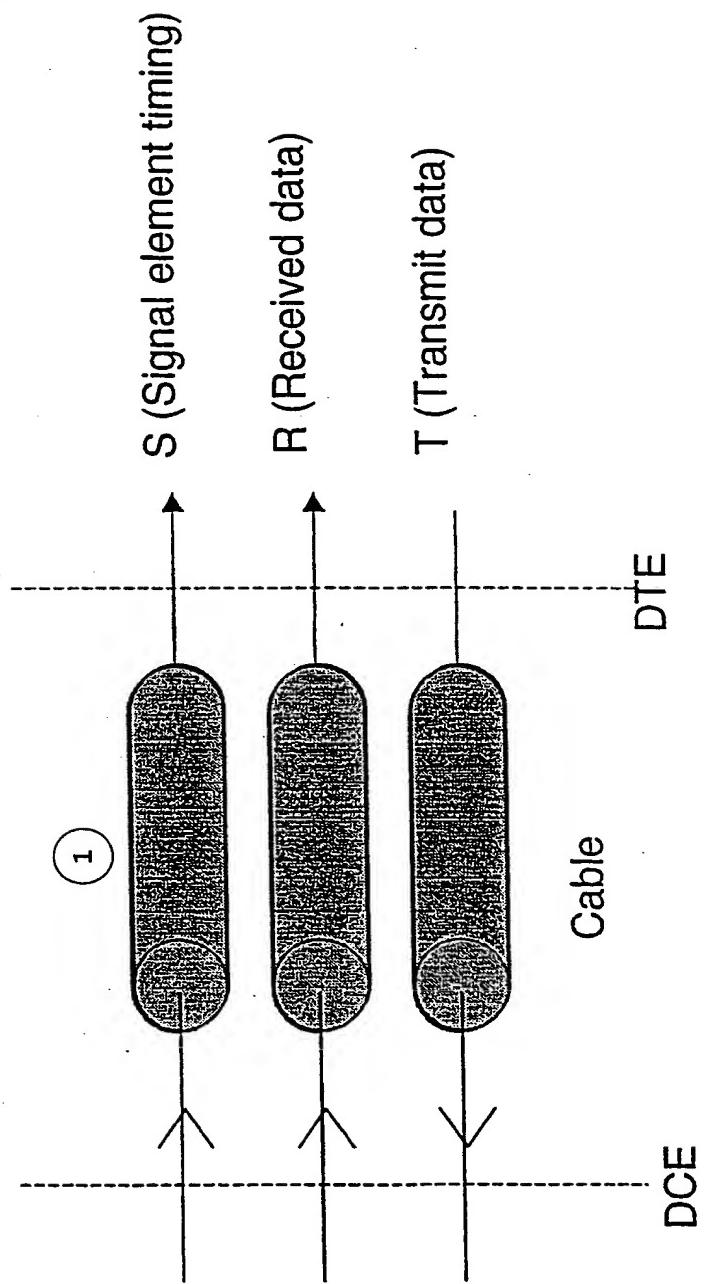
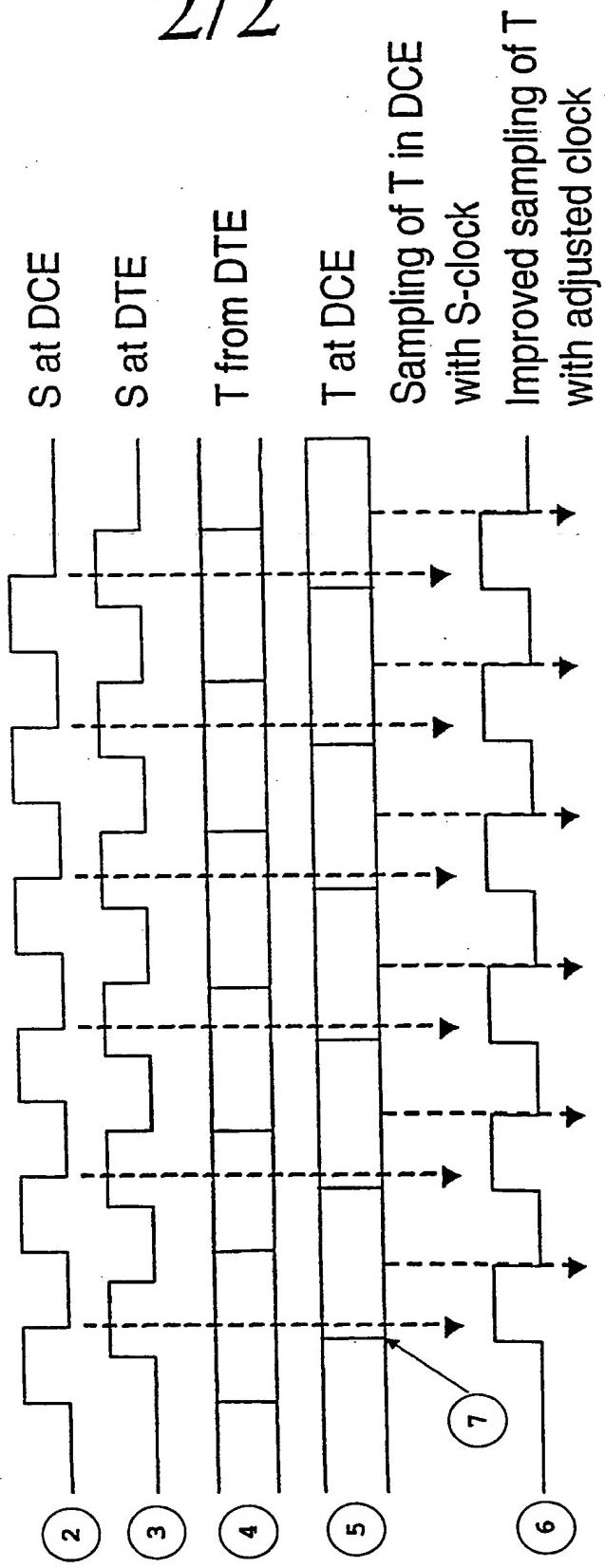


FIG. 1

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X.21 Interface (clock and data)

FIG. 2